a control/status field comprising information for distinguishing between receive and transmit buffer descriptors, and an interrupt bit causing the microengine to send an interrupt to said core processor;

a port number field comprising network and interface specific information;

a data length field comprising the number of bytes containing said underlying information packet read into a buffer;

a data location field comprising a pointer to the beginning of said buffer; and

a user defined data field that is not modified the microengine.

REMARKS

Reconsideration and further examination is respectfully requested.

Applicant amends the claims to more particularly point out and distinctly claim the invention, as well as to place the claims in condition for allowance.

Applicants have made a diligent effort to place the claims in condition for allowance. However, should there remain unresolved issues that require adverse action, it is respectfully requested that the Examiner telephone Daniel A. Rosenberg, Applicants' Attorney at 515-288-2500 so that such issues may be resolved as expeditiously as possible.

For these reasons, and in view of the above amendments, this application is now considered to be in condition for allowance and such action is earnestly solicited.

Respectfully Submitted,

5-25-06
Date

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